IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Joseph R. Little

Serial No.: 09/542,782

Filed: April 4, 2000

For: APPARATUS AND METHOD FOR

FEATURE EDGE DETECTION IN SEMICONDUCTOR PROCESSING

Confirmation No.: 6869

Examiner: S. Yam

Group Art Unit: 2878

Attorney Docket No.: 2269-4298US

(99-0996.00/US)

Notice of Allowance Mailed:

March 22, 2007

VIA ELECTRONIC FILING JUNE 18, 2007

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the specification appear on page 3 of this paper;

Amendments to the claims are set forth in the listing of the claims that begins on page 4 of this paper;

Remarks start at page 14 of this paper.